

REMARKS

Claims 1-8, 13, 14 and 20-31 are pending in this application. By this Amendment, claims 1, 6, 23 and 24 are amended, and claims 30 and 31 are added. No new matter is added.

I. The Claims Define Patentable Subject Matter

A. §102(b) Rejection of Claims 1-5

The Office Action rejects claims 1-5 under 35 U.S.C. §102(b) over U.S. Patent No. 6,229,506 to Dawson et al. (hereinafter "Dawson"). This rejection is respectfully traversed.

Dawson does not teach or suggest the features of claims 1-5. In particular, Dawson does not teach or suggest an electronic circuit that includes "a capacitor that stores a current signal supplied to the capacitor during the first period and a voltage signal supplied to the capacitor during the second period ... no current flowing through the first transistor during at least a part of the second period," as recited in independent claim 1. Nowhere does Dawson teach or suggest this feature.

Thus, for at least these reasons, independent claim 1 is patentable over Dawson. Further, claims 2-5, which depend from claim 1, are also patentable over Dawson for at least the reasons discussed above with respect to claim 1, as well as for the additional features they recite. Withdrawal of the rejection is thus respectfully requested.

B. §103(a) Rejection of Claims 6-8, 13, 14, 20, 22-26, 28 and 29

The Office Action rejects claims 6-8, 13, 14, 20, 22-26, 28 and 29 under 35 U.S.C. §103(a) over Dawson in view of U.S. Patent Application Publication No. 2002/0196215 to Tsuchida et al. (hereinafter "Tsuchida"). This rejection is respectfully traversed.

Dawson and Tsuchida, alone or in a permissible combination, do not teach or suggest the features of independent claim 6. In particular, neither of the applied references teaches or

suggests "a plurality of electro-optical elements that are disposed corresponding to intersections between the plurality of scanning lines and the plurality of data lines, a first electrode that is disposed opposite to a plurality of second electrodes, each of which is included in one electro-optical element of the plurality of electro-optical elements," as recited in independent claim 6 (emphasis added).

Dawson does not teach or suggest this feature. Further, Tsuchida does not remedy the deficiencies of Dawson. In particular, Tsuchida fails to disclose a first electrode that is disposed opposite to a plurality of second electrodes, each of which is included in the one electro-optical element of the plurality of electro-optical elements that are disposed corresponding to intersections between a plurality of scanning lines and a plurality of data lines, as recited in independent claim 6.

Further, neither of the applied references teaches or suggests an electronic circuit that includes "a capacitor that includes a current signal that is received by the electronic circuit during the first period, the capacitor accumulating a voltage signal that is received by the electronic circuit during a second period," as recited in independent claim 23 (emphasis added). Similarly, neither of the applied references teaches or suggests "a capacitor that accumulates a current signal that is received by the electronic circuit in a first mode, the capacitor accumulating a voltage signal that is received by the electronic circuit in a second mode," as recited in independent claim 24 (emphasis added).

Thus, for at least these reasons, independent claims 6, 23 and 24 are patentable over Dawson and Tsuchida. Further, claims 7, 8, 13, 14, 20, 22, 25, 26, 28 and 29, which variously depend from claims 6, 23 and 24, are also patentable over Dawson and Tsuchida, for at least the reasons discussed above with respect to claims 6, 23 and 24, as well as for the additional features they recite. Withdrawal of the rejection is thus respectfully requested.

C. §103(a) Rejection of Claim 21

The Office Action rejects claim 21 under 35 U.S.C. §103(a) over Dawson in view of Tsuchida and further in view U.S. Patent Application Publication No. 2003/0058195 to Adachi et al. (hereinafter "Adachi"). This rejection is respectfully traversed.

Dawson, Tsuchida and Adachi, alone or in a permissible combination, do not teach or suggest the features of claim 21. Adachi does not remedy the deficiencies of Dawson and Tsuchida discussed with respect to claim 1. Tsuchida is only cited by the Office Action for its alleged teaching of a binary data voltage, and Adachi is only cited by the Office Action for its alleged teaching of a multi-value data current. Claim 21 depends from claim 1. Thus, claim 21 is patentable over Dawson, Tsuchida and Adachi for at the reasons discussed above with respect to claim 1, as well as for the additional features it recites. Withdrawal of the rejection is thus respectfully requested.

D. §103(a) Rejection of Claim 27

The Office Action rejects claim 27 under 35 U.S.C. §103(a) over Dawson in view of Tsuchida and further in view of U.S. Patent Application Publication No. 2002/0171607 to Senda et al. (hereinafter "Senda"). This rejection is respectfully traversed.

Dawson, Tsuchida and Senda, alone or in permissible combination, do not teach or suggest the features of claim 27. Senda does not remedy the deficiencies of Dawson and Tsuchida discussed above with respect to claim 24. Senda is only cited by the Office Action for its alleged teaching of power consumption in a second mode, which is lower than the power consumption in a first mode. Claim 27 depends from 24. Thus, claim 27 is patentable over Dawson, Tsuchida and Senda for at least the reasons discussed above with respect to claim 24, as well as for the additional features it recites. Withdrawal of the rejection is thus respectfully requested.

II. Conclusion

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of pending claims are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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Attachments:

Request for Continued Examination
Amendment Transmittal

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